Student	Magana	Mazanac	Miller	Montalvo	Williams
Microprocessor	8048/8051	PDP-8	8080	1802	4004
Background of original chip (inc. year, feature size, area, clock, power, # transistors..) 5: Complete 4: Missing a few details 3: Missing some major aspect 2: Rather vague 1: Not given					
Overview Instruction set 5: Complete 4: Missing a few details 3: Missing some major aspect 2: Rather vague 1: Not given					
Original microarchitcture & off-chip 5: Complete 4: Missing a few details 3: Missing some major aspect 2: Rather vague 1: Not given					
Datapath Slice & Transistor Est. 5: Complete 4: Missing a few details 3: Missing some major aspect 2: Rather vague 1: Not given					
Dennard Scaling 5: Complete 4: Missing a few details 3: Missing some major aspect 2: Rather vague 1: Not given					
Constant Voltage Scaling 5: Complete 4: Missing a few details 3: Missing some major aspect 2: Rather vague 1: Not given					
Presentation time 5: Way too long 4: A bit long 3: Just right 2: A bit short 1: Significantly too short					
Overall presentation 5: Way too much detail 4: A bit much detail 3: Just right 2: A bit light on detail 1: Significantly missing in detail					
Speaking rate 5: Way too fast 4: A bit fast 3: Just right 2: A bit too slow 1: Significantly too slow					
Comments					